Contact

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Top Skills

C++

Performance Improvement

Multithreading

Cedomir Rebic

Senior Software Engineer / C++ | Performance & Real-time Systems
• Cross-platform (Linux/Windows)
London, England, United Kingdom

Summary

C++ developer with 25+ years' experience building performance-sensitive, real-time and cross-platform systems. I specialise in modern C++ (up to C++20), multithreading and low-latency techniques, performance and memory optimisation, and pragmatic system architecture. I've worked extensively in mobile/telecom, analytics and cross-platform product teams (Symbian → Nokia → Accenture; Verint; Helvar), delivering features, leading performance re-designs and supporting production incidents. Typical responsibilities I enjoy: feature design & implementation, multithreaded architecture, profiling & optimization, and mentoring/ tech lead tasks.

I'm actively expanding my Kotlin experience (professional use since mid-2024) while continuing to use C++ as my primary language.

Tech snapshot: C++ (25+ yrs, up to C++20) · Multithreading, real-time, performance improvement & low-latency · WebSockets · Windows & Linux development · Git · Kotlin (since 2024)

Experience

Helvar

Senior Software Engineer
June 2023 - Present (2 years 9 months)
London Area, United Kingdom

• Develop and maintain cross-platform DALI lighting control software in C++ for Windows and Linux environments.
• Led end-to-end delivery of the "extension sensors support" feature for the flagship Linux DALI controller — design, implementation, integration and release.
• Own and maintain the HelvarNet component, ensuring reliability, scalability and performance across the product lifecycle.

• Contribute to core system architecture for mission-critical lighting control; focus on robustness, thread-safety and deterministic behaviour.
• Write and maintain unit/integration tests, support release testing, and assist with customer escalations and field issues.

Verint
Senior Software Engineer
November 2012 - February 2023 (10 years 4 months)
Weybridge, England, United Kingdom

• Lead developer for RTSA (Real-time Speech Analytics) within Verint's Workforce Optimization product — design, implementation and maintenance of features used by major banks and large enterprises worldwide.
• Contributed to technical design and architecture of new features and platform integrations.
• Led performance and sizing optimisation to meet real-time SLAs: reworked multithreaded models, reduced synchronization overhead, replaced hot-path data structures with more efficient C++ alternatives, and reduced memory usage.
• Implemented and maintained new functionality and critical bug fixes in C++ (feature work, refactors and performance improvements).
• Wrote and maintained gtest unit suites and participated in release-phase testing and validation.
• Supported Tier-3 customer escalations and provided technical analysis and fixes for production issues.

Accenture
Senior Software Engineer / Project Tech Lead / Error Manager
February 2011 - April 2012 (1 year 3 months)

• Remained in the same engineering team as Nokia's (ex-Symbian) department was transferred to Accenture in 2011.
• Managed a team of 10 engineers responsible for triage and resolution of C++ errors in the Telephony / Packet Services area of the Symbian OS: ran daily stand-ups, assigned and prioritised defects, and led failure-report analysis and corrective planning.
• Drove performance improvements and feature development — optimised C++ code paths and fixed high-impact defects to improve stability and reduce customer-facing issues.
• Produced error-trend reports and recommended resource reallocation and process changes to reduce recurrence.

Nokia
Senior Software Engineer / Project Tech Lead
February 2009 - February 2011 (2 years 1 month)
London Area, United Kingdom

• Continued in the same engineering team following Nokia's acquisition of
Symbian (2009).
• Developed new features, optimised C++ source and fixed bugs in
Telephony / Packet Services of Symbian OS.
• Contributed to the SHAI (Hardware Abstraction Interface) project —
participated in architecture and redesign discussions and integration camps
with external hardware partners (ST-Ericsson, Fujitsu, Atelier, QuIC) to define
a vendor-facing standard.
• Worked on an experimental cross-platform project using RhoMobile and on
platform integration activities.

Symbian
Senior Software Engineer
January 2006 - February 2009 (3 years 2 months)
London Area, United Kingdom

• Core member of the Telephony / Packet Services engineering team (team
continued under Nokia after the 2009 acquisition).
• Implemented and optimised features in Symbian OS: dual-core platform
support, Dual-SIM functionality (received Official Commendation), and
Telephony/packet data components.
• Implemented GPS component in C++ using the SiRF platform and ported
SiRF libraries to Symbian as part of a consultancy engagement.
• Maintained the team development branch and managed smooth integration
to the Master Code Line; took part in multiple integration camps with Freescale
and other partners.

Openwave Systems
Software Engineer
September 2004 - December 2005 (1 year 4 months)
Warrington, England, United Kingdom

• Contract role; seconded to Qualcomm for on-site handset integration (May–
Aug 2005).
• Architected, designed and implemented smartphone software components
for the BREW platform.
• Developed an IM client and the socket/device layer for the Siemens SXG75
handset (BREW).

• Performed platform porting, debugging and performance tuning to meet handset OEM requirements.

Qualcomm
Software Engineer
May 2005 - August 2005 (4 months)
San Diego, California, United States

• On-site integration engineer for the Siemens SXG75 (BREW): integrated the IM client and collaborated with handset/platform teams.
• Performed platform validation, interoperability testing and bug triage to support product rollout.

MicronasNIT
Software Engineer
August 2002 - September 2004 (2 years 2 months)
Novi Sad Metropolitan Area

• Designed and implemented VoIP systems and protocols: SIP terminal/server, H.323 terminal, USB H.323 gateway and H.323 Windows TAPI (TSPI/TSP/MSP).
• Core developer on a complete enterprise Call Centre (ITU-T Q.71): responsible for VoIP domain and integration of ISDN, H.323 and PSTN.
• Delivered protocol implementations and tools for testing/diagnostics (Wireshark traces, signalling/Q.931 analysis).

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Education

University of Novi Sad, Faculty of Technical Sciences
Master's (integrated 5-year BSc+MSc program) degree in Electrical and Computer Engineering, computer networks, software design, hardware-software integration · (2001)